

This project has received funding from the European Union's Horizon 2020 Research and Innovation programme under grant agreement No. 801015



EXA2PRO overview

Dr. Lazaros Papadopoulos **ICCS/NTUA**

EoCoE EXA2PRO workshop



22 February 2021



EXA2PRO at a Glance

Call identifier: H2020-FETHPC-2017
 Topic: FETHPC-02-2017 - Transition to Exascale Computing
 8 partners from 5 countries
 Budget: 3,475,223 €
 Duration: 36 months

The project officially ends on 30 July 2021







EXA2PRO motivation and objectives



STRATEGIC Research Agfnna

exde

Enhancing Programmability and boosting Performance Portability for Exascale Computing Systems

Need to support the growth of HPC applications and their transition to exascale.

Currently, **only few applications** are expected to benefit from upcoming exascale computing systems

Main goal

How?

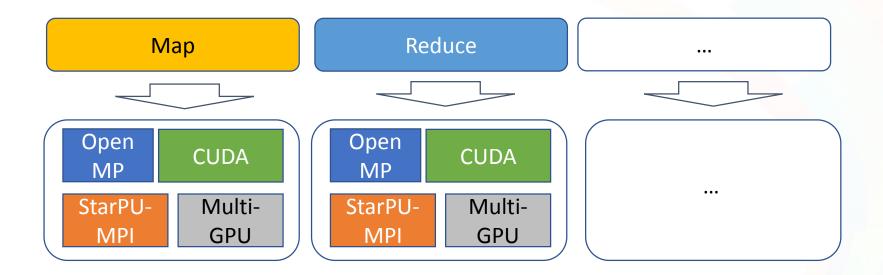
Motivation

Promote **and lower the barrier of access to exascale computing systems** to scientific community and industry

- Exascale systems are expected to be heterogeneous
- EXA2PRO improves the programmability of heterogeneous computing systems by **hiding their complexity** from application developers



• **Objective 1:** Follow a single-source multiple-backend approach



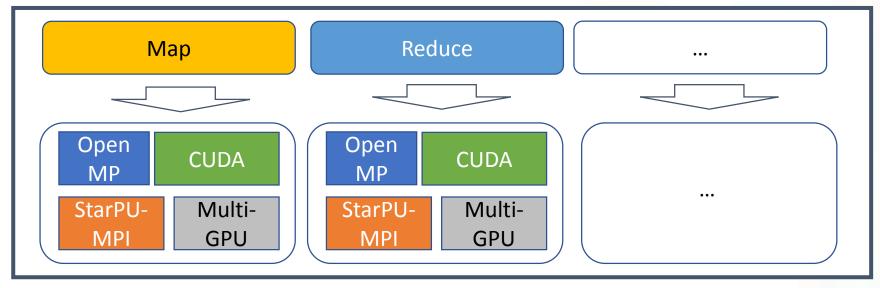
Patterns of computation implemented for a variety of backends (Skeletons)





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SkePU EXA2PRO high-level interface

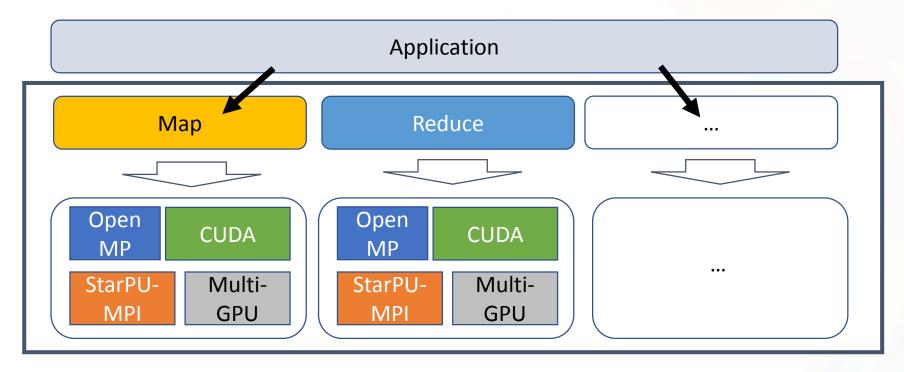


Patterns of computation implemented for a variety of backends (Skeletons)





• **Objective 1:** Follow a single-source multiple-backend approach



- Patterns of computation implemented for a variety of backends (Skeletons)
- Developers are expected to implement computations of their application through the SkePU EXA2PRO tool



• Objective 1: Single-source multiple-backend approach

• Advantages for application developers:

 Experiment with different platforms and accelerators (OpenMP, CUDA, OpenCL-enabled)





Objective 1: Single-source multiple-backend approach

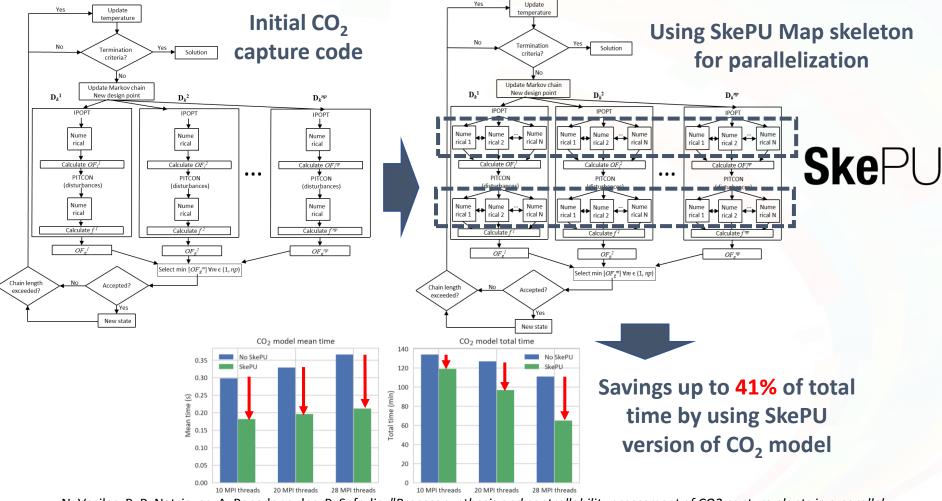
• Advantages for application developers:

- Experiment with different platforms and accelerators (OpenMP, CUDA, OpenCL-enabled)
- Can directly use the backend implementations already provided by EXA2PRO SkePU tool. → Productivity





Using EXA2PRO tools to advance the design of material and processes for CO₂ capture

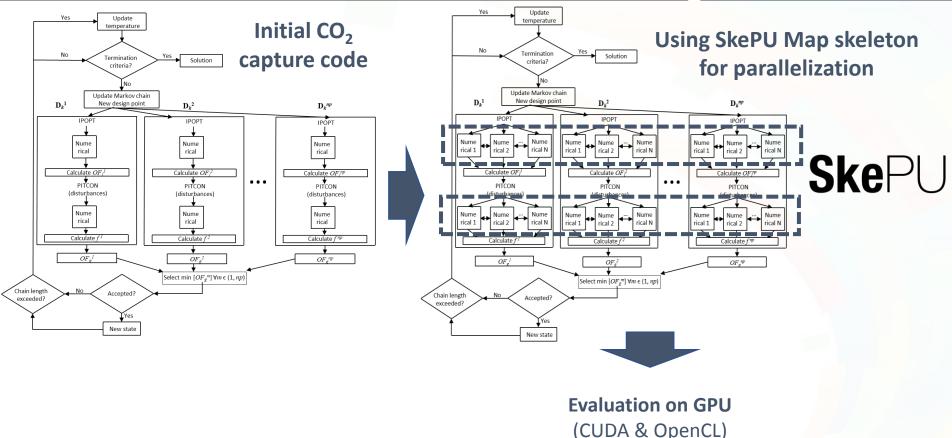


N. Vasilas, P. P. Natsiavas, A. Papadopoulos, P. Seferlis, "Process synthesis and controllability assessment of CO2 capture plants in a parallel environment", In Proc. 22nd Conference on Process Integration, Modelling and Optimisation for Energy Saving and Pollution Reduction, 2019.





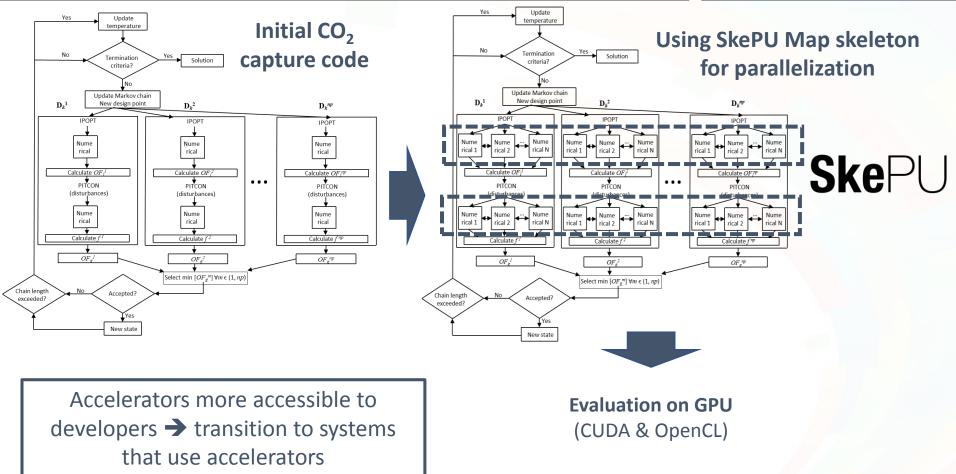
Using EXA2PRO tools to advance the design of material and processes for CO₂ capture



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Using EXA2PRO tools to advance the design of material and processes for CO₂ capture



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Objective 1: Single-source multiple-backend approach

• Advantages for application developers:

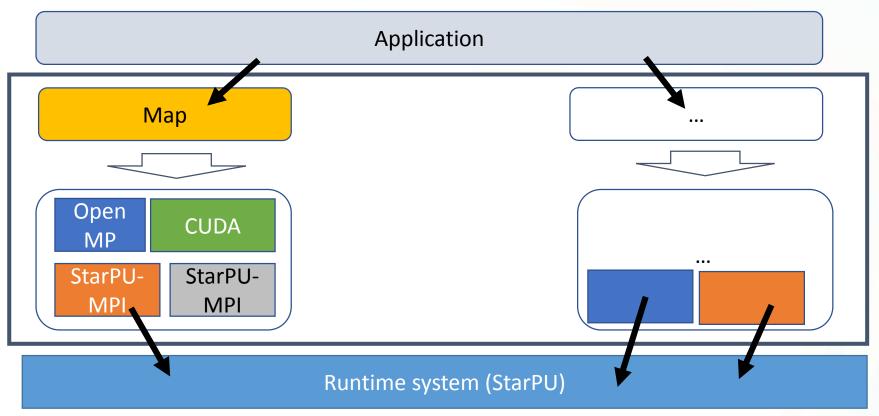
- Experiment with different platforms and accelerators (OpenMP, CUDA, OpenCL-enabled)
- Can directly use the backend implementations already provided by EXA2PRO SkePU tool.
 Productivity

• The effort to apply the EXA2PRO interface is lower when:

- The application is developed in C/C++
- The computation kernel is well-isolated



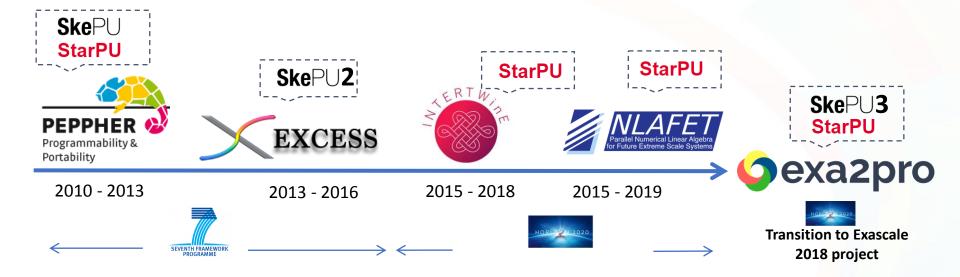
• **Objective 1:** Follow a single-source multiple-backend approach



- Elimination of suboptimal or non-applicable backend implementations at design time
- The runtime system selects the most efficient implementation at runtime considering performance and energy as optimization criteria



•Objective 2: Co-design approach between tools and applications



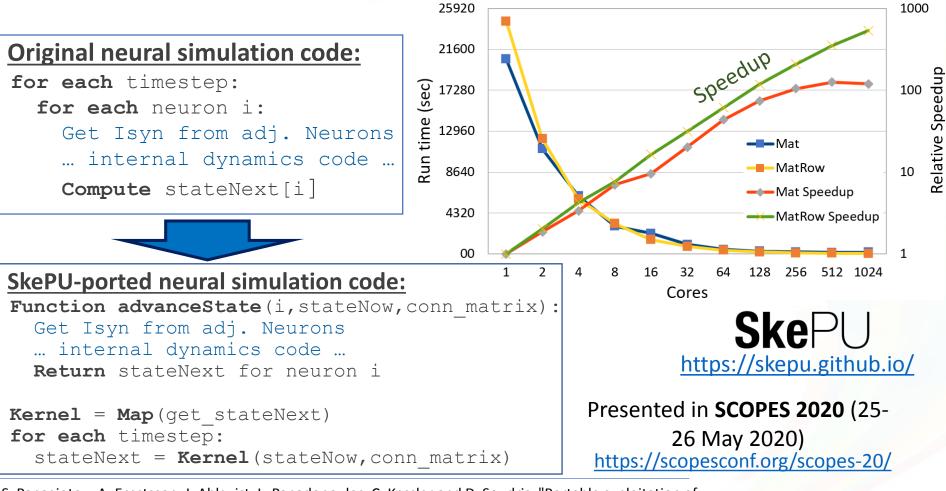
Tools such as SkePU and StarPU evolve towards exascale computing





Multi-node neural simulations using SkePU

Scaling demonstration across multiple nodes using MatRow SkePU data container (Tetralith cluster: 32 nodes used, 32 cores per node)



S. Panagiotou, A. Ernstsson, J. Ahlqvist, L. Papadopoulos, C. Kessler and D. Soudris, "Portable exploitation of parallel and heterogeneous HPC architectures in neural simulation using SkePU", SCOPES 2020.



•Objective 3: Evaluation the data-flow programming model

 Used to program the FPGA-based MAXELER data-flow engines (DFEs)





Results: Metalwalls (Simulator for supercapacitors)

HiPEAC 2021

Initial results of porting Metalwalls on DFE:

4x performance gain10x lower energy consumption50% higher performance/watt than P100 GPU



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<u>Development time:</u> 8 – 12 months → Tools to improve productivity for accessing accelerators





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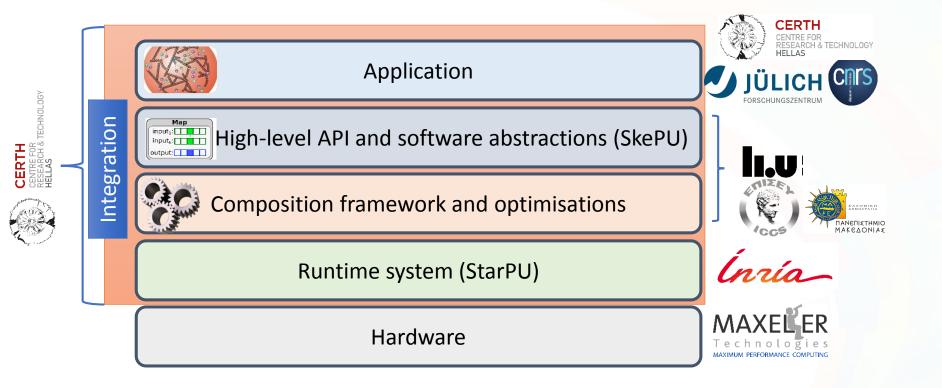
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Next step: Use Metalwalls with StarPU for multi-DFE experiments





The EXA2PRO stack

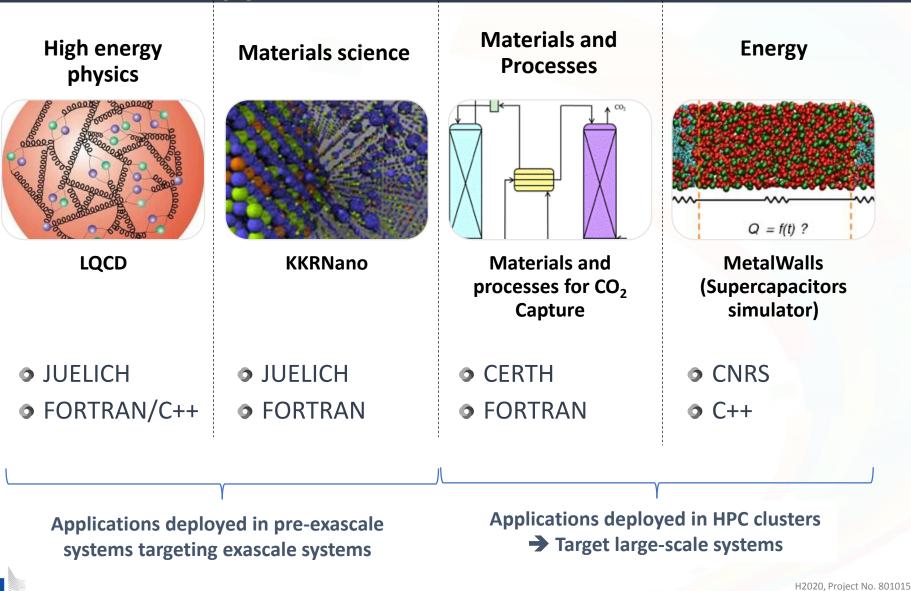




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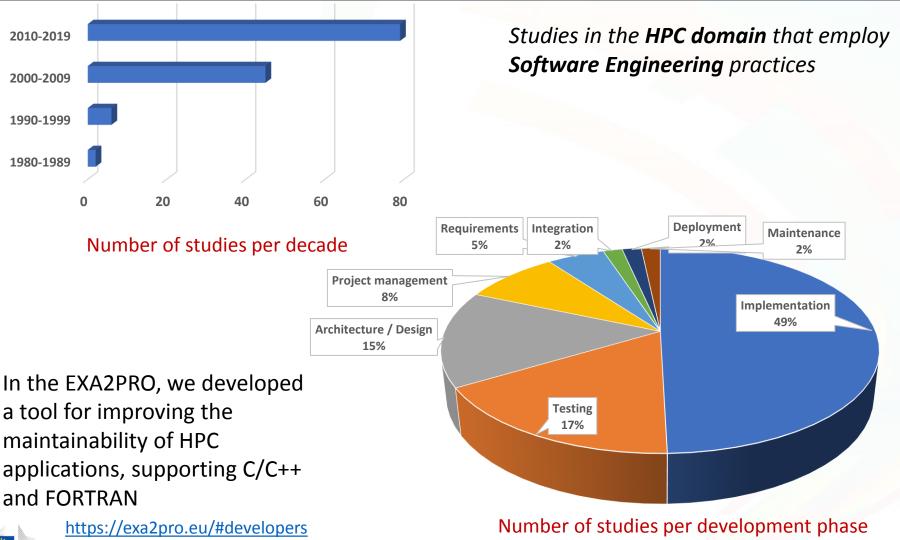
EXA2PRO applications



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Other aspects: Software engineering practices in HPC





Conclusions and lessons-learned

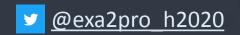
- Tool support for accessing accelerators can significantly improve the productivity of application developers
- Interaction between tool developers and application providers was very constructive
 - Contributed to the maturity of the tools and to the application performance improvement
- Data-flow programming is difficult but initial results are very promising



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Thank You!

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