

Workshop Accelerated Computing For Fusion

Tuesday, November 29, 2016

Second day, morning - 34 (9:00 AM - 12:00 PM)

time	[id] title	presenter
9:00 AM	Welcome address (2)	
9:30 AM	[5] Optimization of fusion kernels on accelerators (remote from Japan)	Dr ASAH, Yuuichi
10:00 AM	[6] HPC Architectures Evolution : the case of Marconi, the new CINECA flagship system	Dr LANUCARA, Piero
10:30 AM	[7] Efficient Parallel Programming on Xeon Phi for Exascale	Dr PETIT, Eric
11:00 AM	Short break	
11:30 AM	[8] HLST/CINCOMP experience with new architectures : moving from helios to marconi	Dr MOCHALSKYY, Serhiy