

## **Pregramme des sessions**

**28-29 nov. 2016**

# **Workshop Accelerated Computing For Fusion**

## **Second day, morning**

Maidon de la Simulation, 34  
Maison de la Simulation USR 3441 Bâtiment 565 - Digiteo CEA Saclay 91191 Gif-sur-Yvette cedex

# mar. 29 novembre

09:00

## Second day, morning

### Session |

**Site:** Maison de la Simulation, 34, Maison de la Simulation USR 3441 Bâtiment 565 - Digiteo CEA Saclay 91191 Gif-sur-Yvette cedex

09:00 - 09:30

### Welcome address (2)

09:30 - 10:00

### Optimization of fusion kernels on accelerators (remote from Japan)

#### Orateur

Dr Yuuichi ASAHI

10:00 - 10:30

### HPC Architectures Evolution : the case of Marconi, the new CINECA flagship system

#### Orateur

Dr Piero LANUCARA

10:30 - 11:00

### Efficient Parallel Programming on Xeon Phi for Exascale

#### Orateur

Dr Eric PETIT

11:00 - 11:30

### Short break

11:30 - 12:00

### HLST/CINCOMP experience with new architectures : moving from helios to marconi

#### Orateur

Dr Serhiy MOCHALSKYY

12:00