

3rd Accelerated Computing For Fusion Workshop, Maison de la Simulation, Saclay, France, November 28-29, 2016  
 Agenda version 9

n°g	n°l	Day	starts	ends	duration	speaker_fn	speaker_In	organisation	title
		Monday November 28	15:00	15:10	00:10			MdIS or CEA	<b>Welcome address (1)</b>
1	1	Monday November 28	15:10	15:40	00:30	Guillaume	COLIN de VERDIERE	CEA/DIF	Processors Evolution
2	2	Monday November 28	15:40	16:25	00:45	Serge	PETITON	CNRS & MdIS	Towards Intelligent Linear Algebra Methods and Multi-Level Computing
		Monday November 28	16:25	16:45	00:20				<b>Discussions &amp; wrap-up</b>
			16:45						<b>End of first day</b>
		Tuesday November 29	09:00	09:30	00:30			MdIS or CEA	<b>Welcome address (2)</b>
3		Tuesday November 29	09:30	10:00	00:30	Yuuichi	ASAHI	CEA & JAEA	Optimization of fusion kernels on accelerators (remote)
4	1	Tuesday November 29	10:00	10:30	00:30	Piero	LANUCARA	CINECA	HPC Architectures Evolution : the case of Marconi, the new CINECA flagship system
5	2	Tuesday November 29	10:30	11:00	00:30	Eric	PETIT	INTEL	Efficient Parallel Programming on Xeon Phi for Exascale
			11:00	11:30	00:30				<b>Short break</b>
6	3	Tuesday November 29	11:30	12:00	00:30	Serhiy	MOCHALSKYY	HLST	HLST/CINCOMP experience with new architectures : moving from helios to marconi
			12:00	14:00	02:00				<b>Lunch</b>
7	1	Tuesday November 29	14:00	14:30	00:30	Gabriel	HAUTREUX	GENCI	French accelerator technological watch experience
8	2	Tuesday November 29	14:30	15:00	00:30	Francois	COURTEILLE	NVIDIA	NVIDIA accelerators experience and vision
9	3	Tuesday November 29	15:00	15:30	00:30	Jean-Pierre	PANZIERA	ATOS/Bull	BULL experience with accelerators and Exascale Vision
			15:30	16:00	00:30				<b>Short break</b>
4		Tuesday November 29	16:00	16:45	00:45				<b>Panel : new issues and future directions in accelerated HPC</b>
		Tuesday November 29	16:45	17:00	00:15				<b>Discussions &amp; wrap-up</b>
			17:00						<b>End of second day / End of Workshop</b>